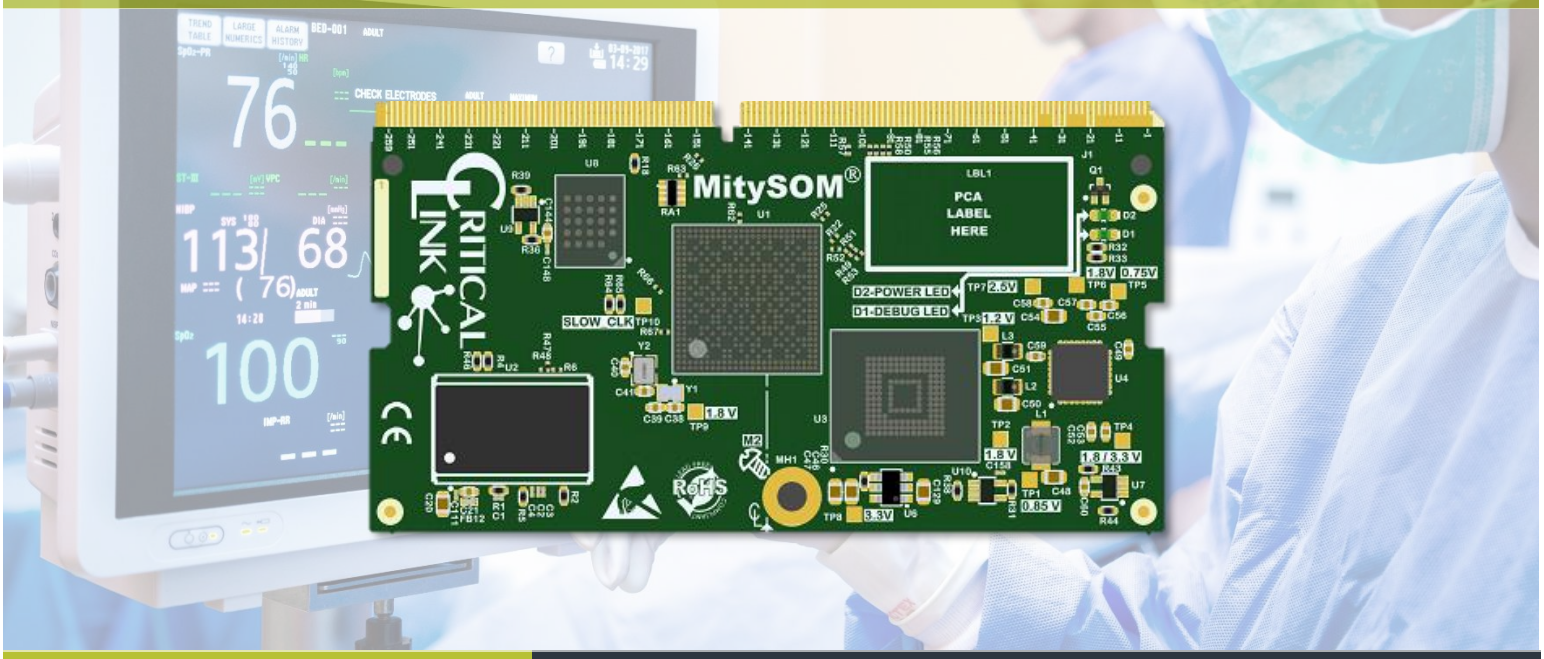
MitySOM-AM62

TI Sitara AM62xx Modules



Production-Ready Solution for Broad Market Applications

- Human Machine Interfaces
- Medical Equipment
- Embedded Instrumentation
- Retail Automation
- Driver Monitoring System
- Telematics Control Unit
- Vehicle to Infrastructure / Vehicle to Vehicle (V2X / V2V)
- 3D Re-configurable automotive instrument cluster
- Appliance user interface & connectivity

SOM SPECIFICATIONS

- TI AM62x Application Processor
  - Up to Quad Arm Cortex-A53 MPU at up to 1.4 GHz
  - 32 KB L1 Program Cache per Core
  - 32 KB L1 Data Cache per Core
  - 512 KB Shared L2 Cache
  - Advanced SIMD and floating-point extension (Arm Neon)
  - JTAG Emulation/Debug
- Single Core Arm Cortex-M4F MCU up to 400 MHz with 256 KB SRAM
- Dual Core Programmable Real Time Unit (PRU) up to 333 MHz
- 3D Graphics Processing Unit supporting OpenGL ES 3.x
- AM62x Processor Choices
  - AM6254 (Quad Core, 3D)
  - AM6252 (Dual Core, 3D)
  - AM6251 (Single Core, 3D)
  - AM6234 (Quad Core)
  - AM6232 (Dual Core)
  - AM6231 (Single Core)
- Up To 4 GB DDR4 CPU RAM
  - 16 bits wide, 3.2 GB/sec
- Up to 128 GB eMMC FLASH
  - 8 bits wide, HS200 speed
- Up to 256 MB Octal/Quad SPI NOR FLASH

STANDARD DDR4 SO-DIMM-260 INTERFACE

- 2 10/100/1000 Mbps EMACs
- 1 MIPI CSI 1.3 Camera Input, 4 lanes up to 2.5 Gbps
- Dual Display Support
  - Up to 1080p for each display
  - 1 24-bit RGB output
  - 2x4 lanes OLDI/LVDS
- 3 CAN ports
- 7 UARTs
- 2 USB 2.0 Dual Role Device Ports
- 3 multi-Channel MCASPs
- 2 4 lane MMC/SD/SDIO
- 3 SPI, 3 I2C, GPIO
- eHRPWM, eQEP
- Single 3.3V Input Power Supply

6712 Brooklawn Parkway, Syracuse NY 13211
Tel 315.425.4045 info@criticallink.com

WWW.CRITICALLINK.COM